INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)	Application Number
13691	09/870,767
Applicant(s) Richard S. NORMAN et al.	
Filing Date June 1, 2001	**Group Art Unit** ~~2661~~ 2664

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AM	1.	U.S. 5,790,539	08-04-1998	Chao et al.	—	—	
	2.	U.S. 5,831,980	11-03-1998	Varma et al.	—	—	
	3.	U.S. 6,069,895	05-30-2000	Ayandeh	—	—	

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AM	4.	EP 0 241 152 A2	10-14-1987	EUROPE	—	—		

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AM	5.	NOTANI H ET AL: "An 8*8 ATM switch LSI with shared multi-buffer architecture" PROCEEDINGS OF THE SYMPOSIUM ON VLSI CIRCUITS. SEATTLE, JUNE 4-6, 1992, SYMPOSIUM ON VLSI CIRCUITS, NEW YORK, IEEE, US, 4 June 1992 (1992-06-04), pages 74-75, XP010064987
AM	6.	International Search Report PCT/CA02/00810, August 1, 2003

EXAMINER	DATE CONSIDERED
ASIT PATEL	8/5/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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↓	2.	EP 1 051 001	08-11-2000	EUROPE	—	✓		
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AP	3.	Annex to FORM PCT/ISA/206, PCT/CA02/00810

EXAMINER	DATE CONSIDERED
AJIT PATEL	8/5/05

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Richard S. Norman et al.	
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EXAMINER	DATE CONSIDERED
ASIT PATEL	8/5/04

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INFORMATION DISCLOSURE CITATION

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Docket Number (Optional)	Application Number
13691	09/870,767
Applicant(s) Richard NORMAN et al.	
Filing Date June 1, 2001	**Group Art Unit** ~~2661~~ 2664

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INFORMATION DISCLOSURE CITATION *(Use several sheets if necessary)*	Docket Number (Optional) 13691	Application Number 09/870,767
	Applicant(s) Richard NORMAN et al.	
	Filing Date June 1, 2001	Group Art Unit ~~2661~~ 2664

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	6.	Werner Bux et al.; Technologies and Building Blocks for Fast Packet Forwarding; IEEE Communications Magazine; January 2001; pp. 70-77.
	7.	Minagawa, N. et al. ; Dept. of Comput. Scil, University of Electro-Commun. Tokyo, Japan; Implementation of a network switch on chips;(Abstract) Communications, vol. 13, no. 1; retreived on March 16, 2001 from INSPEC database.
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	12.	A New Architecture for Switch and Router Design; PMC-Sierra Inc.; December 22, 1999; Internet URL http://www.pmcsierra.com/pressRoom/pht/1cs_wp.pdf retrieved on July 4, 2001; pp. 1-8;
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13[illegible]1	09/870,767
Applicant(s)	
Richard S. NORMAN et al.	
Filing Date	**Group Art Unit**
6/1/01	2664

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	2.	U.S. Re. 34,755	11/10/1994	Eng et al.	—	—	
	3.	U.S. Re. 34,811	27/12/1994	Eng et al.	—	—	
	4.	U.S. 5,361,257	01/11/1994	Petersen	—	—	
	5.	U.S. 5,467,347	14/11/1995	Petersen	—	—	
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	7.	U.S. 5,787,084	28/07/1998	Hoang et al.	—	—	
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	9.	U.S. 6,144,662	07/11/2000	Colmant et al.	—	—	
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EXAMINER	DATE CONSIDERED
ASIT PATEL	8/15/01

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INFORMATION DISCLOSURE CITATION

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ATTY DOCKET NO.	SERIAL NO.
1368	09/870,767
APPLICANT(S) Richard S. NORMAN et al.	
FILING DATE 6/1/01	GROUP 2864

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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AP	14.	U.S. 6,111,856	29/08/2000	Huterer et al.	—	~	
AP	15.	U.S. 5,859,835	12/01/1999	Varma et al.	—	✓	

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EXAMINER	DATE CONSIDERED
ASIT PATEL	8/01/05

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